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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081665	FILING DATE 02/21/2002	CLASS 216	SUBCLASS 729	GAU 4746	EXAMINER <i>M. H. Kachar</i>
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**APPLICANTS: Chen Wen-Ming; Wang Wen-Chi; 1763

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		67,200-662	
Verified and Acknowledged Examiners's initials			
TITLE : Microchip fabrication chamber wafer detection			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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